Brad Devices — Prospectus

Kisumu, Kenya · Festus Bradley Nyadimo ("The Architect") — founder, sole builder *Software before silicon. Receipts after that.*

We design the whole machine, and we ship the software that builds it. One vertically integrated computing ecosystem — CPU, GPU, NPU, memory, storage, operating system, drivers, developer toolchain — architected as a single plan from a single roof in Kisumu, Kenya. The software runs today. The silicon is specified but not fabricated. This page keeps those two statements apart.

The problem: nobody owns the whole machine

The stack is closed in layers and nobody is accountable across them. Instruction sets belong to one set of companies, memory controllers to another, operating systems to a third, drivers to a fourth. Every boundary was reasonable when it was drawn.

So a decade of compute progress went into faster clouds, faster cryptography and faster inference rental — rent, not ownership. Nothing new has been *owned* at the machine level in a very long time, and a country that imports every layer has no leverage at any of them.

African countries are praised for fintech and agritech because those are the doors the West already believes are open. Designing instruction sets, memory controllers and operating systems does not go through those doors. It builds new ones.

What exists today — receipts

- **15 / 15** ctest green. Every test is a receipt, not a claim.
- **`v0.1.0` released** — `braddev` and `bradc` for linux-x86_64, linux-aarch64, macos-arm64. **9 artifacts**, each with a SHA-256. The release job refuses to publish unless the suite is green, so a downloadable binary and a passing test run are the same event.
- **145 reference documents, 36,726 lines**, cross-linked and self-consistent.
- **1,737-line unified specification**, 41 pages compiled, public and free.
- **Shipped:** assembler, 32-lane SIMD interpreter, one-binary developer CLI, debugger, cycle-level profiler, reference drivers, BVML/BVN math and neural libraries with exact-value tests.
- **Specified and public:** two deliberately separate instruction sets — BradISA (CPU) and BradVector (GPU) — meeting on the BradFusion fabric, sharing SPMP memory, sharing no instructions.

What is designed, not built

No Brad Devices silicon has been fabricated. No processor of ours exists in metal. No foundry, no fabrication partner, no price. The instruction sets are `DESIGNED`; the toolchain is `SHIPPED`. A reviewer should read that here rather than discover it later.

What two years would buy

Fabrication is the step that turns `DESIGNED` into `SHIPPED`, and it is the step money actually closes — no engineering substitutes for a mask set and a wafer.

1. **A fabrication run** — mask set, wafers, packaging, bring-up, first silicon. *(Costs to be inserted from real foundry quotes.)*
2. **The first part that boots**, trading a class of assumptions for measurements.
3. **The platform completed across every layer** — memory, storage, drivers, runtime — so an integrated stack is coherent rather than merely separately documented.
4. **No equity is requested.** The fellowship takes none.

The team

One builder. Fifteen, in Kisumu, self-taught. No degree, no co-founder, no institution, no external funding. Every artifact above came from one person in one repository whose history is public and whose claims are checked by its own CI.

That is either the weakness of this application or the entire point of it.

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Every figure here is generated at build time and verified before publication: if one is wrong, the build fails rather than this page lying.